



No./VIDUNI/EXAM/2025/0711

DATE: 13/11/2025

Exam Notification No.:547/2025

EXAM SCHEDULE

B. TECH. ELECTRICAL ENGG.

Sem-III

December - 2025

The detailed Schedule / Time Table of Institute of Engineering and Technology (Theory) Examination, for the December – 2025 of Vidhyadeep University is hereby declared as per details given below:

DATE & DAY	TIME	SUBJECT CODE	SUBJECT NAME
1/12/2025 Monday	10:30 AM TO 01:00 PM	002391311	Applied Mathematics for Electrical engineering
2/12/2025 Tuesday		002303301	Electrical Circuit Analysis
3/12/2025 Wednesday		002303303	Analog & Digital Electronics
4/12/2025 Thursday		002303305	Electrical Machines – I
5/12/2025 Friday		002303307	Control System Theory

Instruction:

- (1) Candidates are required to be present at their place of examination before 30 minutes of the commencement of the examination.
- (2) Candidate must bring Hall Ticket and valid ID card.
- (3) Candidates must adhere to rules & regulations, Instructions pertaining to University Examination

I/C Controller of
Examinations



Vidhyadeep Campus, At Post – Anita, Kim–Olpad Highway, Ta – Olpad, Dist – Surat, Gujarat, India – 394110.

