



No./VIDUNI/EXAM/2025/0445

DATE: 28/08/2025

Exam Notification No.:0433/2025

EXAM SCHEDULE

B. Tech (Electrical Engineering) Semester – III

September – 2025 (Remedial)

The detailed Schedule / Time Table of B. Tech, Sem – III (Remedial) Examination, for the September –2025 of Vidhyadeep University is hereby declared as per details given below:

DATE & DAY	TIME	SUBJECT CODE	SUBJECT NAME
09/09/2025 Tuesday	10.30 AM	002391311	Applied Mathematics for Electrical Engineering
11/09/2025 Thursday		002303301	Electrical Circuit Analysis
13/09/2025 Saturday		002303303	Analog & Digital Electronics
16/09/2025 Tuesday	01.00 PM	002303305	Electrical Machines – I
18/09/2025 Thursday		002303307	Control System Theory

Instruction:

- (1) Candidates are required to be present at their place of examination before 30 minutes of the commencement of the examination.
- (2) Candidate must bring Hall Ticket and valid ID card.
- (3) Candidates must adhere to rules & regulations, Instructions pertaining to University Examination.

I/C Controller of Examinations



Vidhyadeep Campus, At Post – Anita, Kim–Olpad Highway, Ta – Olpad, Dist – Surat, Gujarat, India – 394110.

